



COPY OF PAPERS  
ORIGINALLY FILED

#4/Electronics  
DKing  
5/7/02 2812

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

TECHNOLOGY CENTER 2800

RECEIVED  
MAY - 1 2002

In re the Application of:

Robert H. Havemann

Docket No.: TI-27506

Serial No.: 10/001,472

Examiner: Gurley, L.

Filed: 11/01/01

Art Unit: 2812

For:

INTEGRATED CIRCUIT HAVING A DOPED POROUS DIELECTRIC  
AND METHOD OF MANUFACTURING THE SAME

ELECTION

Assistant Commissioner for  
Patents  
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(A)  
I hereby certify that the above correspondence is being deposited  
with the U.S. Postal Service as first Class Mail in an envelope  
addressed to Assistant Commissioner for Patents, Washington,  
D.C. 20231 on:

4-18-02

*Marianna Smith*  
Marianna Smith

Dear Sir:

With respect to the Restriction Requirement mailed on 03/28/02, the Examiner has restricted the instant application to the invention of Group I (Claims 11-20), or Group II (Claims 1-10). In light of this, Applicants elect to pursue Group II (Claims 1-10) without traverse.

Respectfully submitted,

Jacqueline J. Garner  
Attorney for Applicant(s)  
Reg. No. 36,144

Texas Instruments Incorporated  
P.O. Box 655474, M/S 3999  
Dallas, TX 75265  
(214) 532-9348 or  
(972) 917-5643  
Fax: (972) 917-4418